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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043027	FILING DATE 01/09/2002	CLASS 438	SUBCLASS 396	GAU 2012	EXAMINER VESPERMAN
**APPLICANTS: Chung Jeong-hee; Park In-sung; Yoo Jae-hyun; 2813					
**CONTINUING DATA VERIFIED: <i>we</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>we</i> REPUBLIC OF KOREA 2001-2960 01/18/2001					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no			5649-917		
Written and Acknowledged Examiners's initials <i>we</i>					
TITLE: Methods of forming an integrated circuit capacitor in which a metal preprocessed layer is formed on an electrode thereof					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Amount Due		Print Fig.	
Date Paid			
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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